

174/212 Cont.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Chong H. Lee et al.  
Application No.: 10/643,276 Confirmation No.: 4060  
Filed : August 18, 2003  
For : PROGRAMMABLE LOGIC DEVICE WITH  
HIGH SPEED SERIAL INTERFACE CIRCUITRY  
Group Art Unit : 2819  
Examiner : Don P. Le

Hon. Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97,  
applicants wish to call the attention of the Examiner to the  
following references:

U.S. Patents

Schlueter et al.	6,240,471 B1	05/29/01
Christopher	6,270,350 B1	08/07/01
Silverman et al.	6,370,603 B1	04/09/02
Ng	6,388,591 B1	05/14/02

Other Documents

"Synchronous High Speed Serial Interface Clocking,"  
IBM Technical Disclosure Bulletin, Vol. 38 No. 04,  
April 1995.

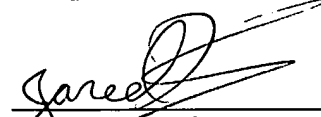
Express Mail No.  
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"High-Speed Serial Interface Micro Channel Adapter,"  
IBM Technical Disclosure Bulletin, Vol. 34, No. 7A,  
December 1991.

These references are also listed on the attached Form  
PTO-1449 (submitted in duplicate), and copies are enclosed.

Consideration of the foregoing in relation to this  
patent application is respectfully requested.

Respectfully submitted,



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FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

DOCKET NO.

AL-212 Cont.

APPLN. NO.

10/643,276

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE  
STATEMENT BY APPLICANTS

APPLICANTS

Chong H. Lee et al.

CONF. NO.

4060

FILING DATE

August 18, 2003

GROUP ART UNIT

2819



## U.S. PATENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,240,471 B1	5/29/01	Schlueter et al.	710	62	
	6,270,350 B1	08/07/01	Christopher	434	69	
	6,370,603 B1	04/09/02	Silverman et al.	710	72	
	6,388,591 B1	05/14/02	Ng	341	100	

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL	
	"Synchronous High Speed Serial Interface Clocking," IBM Technical Disclosure Bulletin, Vol. 38 No. 04, April 1995.
	"High-Speed Serial Interface Micro Channel Adapter," IBM Technical Disclosure Bulletin, Vol. 34, No. 7A, December 1991.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicants.

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